

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hongyong Zhang                      Art Unit : 2814  
Serial No. : 09/362,808                      Examiner : Shrinivas H. Rao  
Filed : July 28, 1999                      Conf. No. : 7320  
Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
**AND SUBMISSION OF CORRECTED FORM PTO-1449**

Supplemental to an information disclosure statement filed with the application on March 4, 2010, applicants submit the attached corrected Form PTO-1449. All of the documents listed on the form PTO-1449 were cited in information disclosure statement filed with the application. The corrected Form PTO-1449 is being submitted to correct an error in the description of the reference identified as Desig. ID "15." Please note that, for this reference, the Document Translation has been changed from "Full" to "ABS." The correction is identified by \*\*'s and bold print.

No fee is believed due at this time. Please apply any charges or credits not associated with the filing fee to deposit account 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

9/5/10



John F. Hayden  
Reg. No. 37640

**Customer No.: 26171**  
Fish & Richardson P.C.  
1425 K Street, N.W.  
11th Floor  
Washington, DC 20005-3500  
Telephone: (202) 783-5070  
Facsimile: (877) 769-7945